

Fig-1A, A New 6-T nMOS Dual Port SRAM

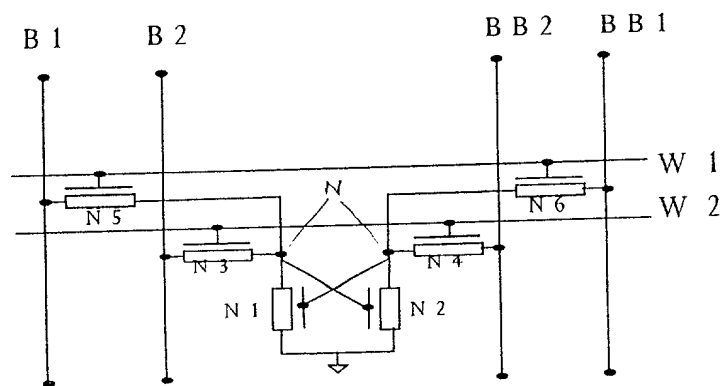


Fig.1 B, A New 7-T Dual Port SRAM

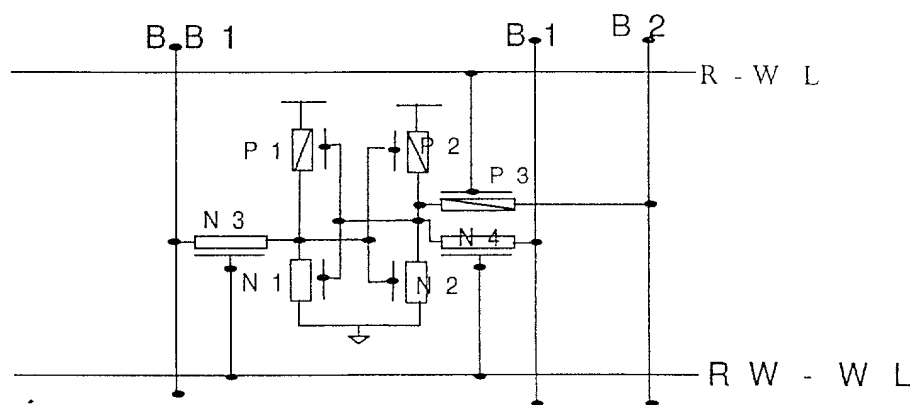


Figure 1 shows the layout of the 1.3 x 1.9 x 2.47 (PR10R ART) SRAM cell. The layout is shown in the top view of the die. The layout is shown in the top view of the die.

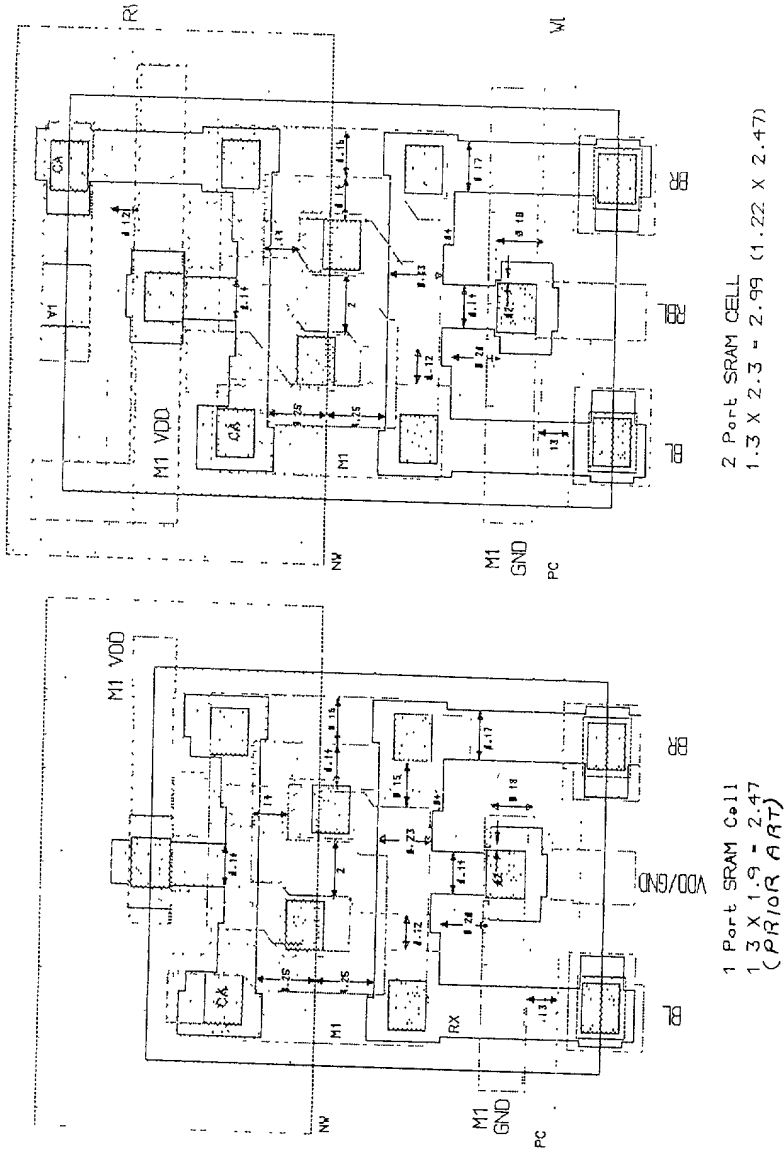


Fig. 1C

Fig.2 Dual-Port RAM, Interleave^d Write Operation

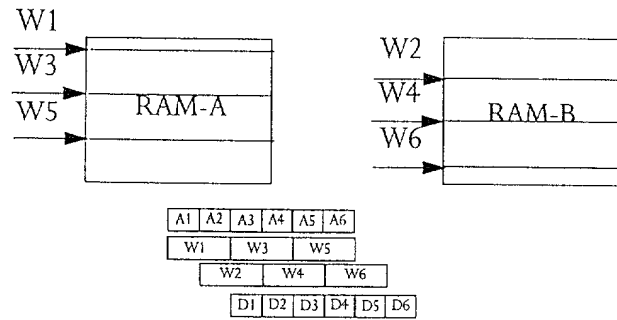


Fig.3 Dual-Port RAM, Interleave^d Read Operation

